

TOP SECRET

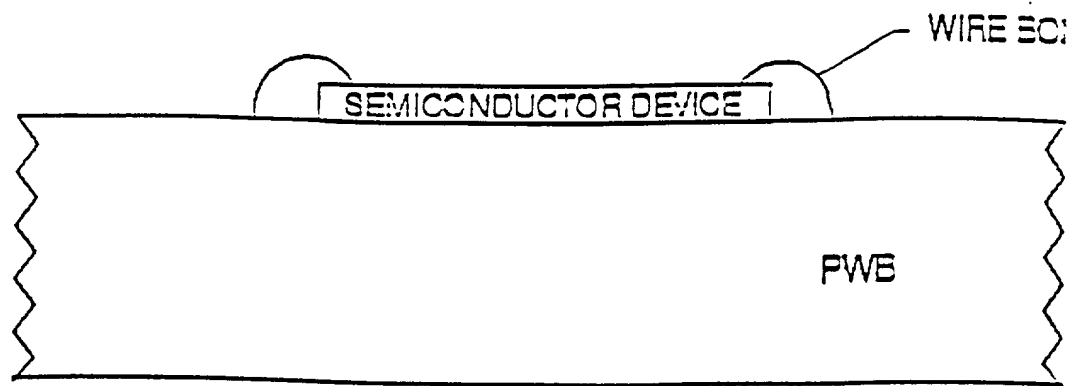


Fig. 1

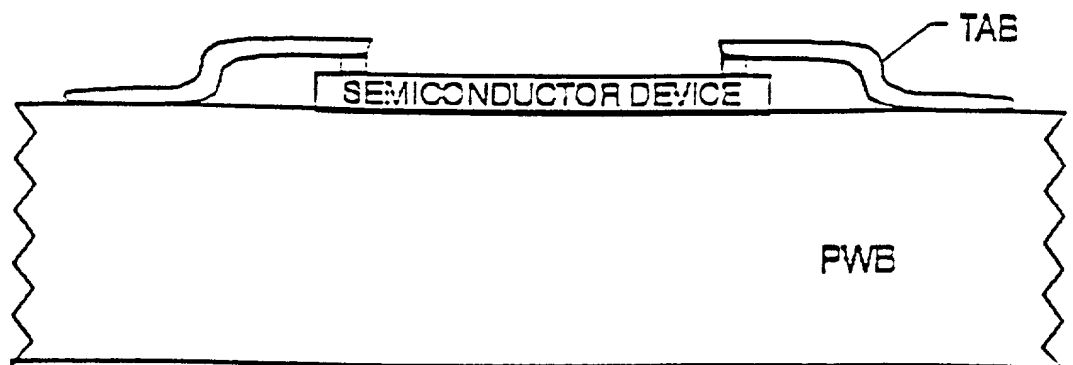


Fig. 2

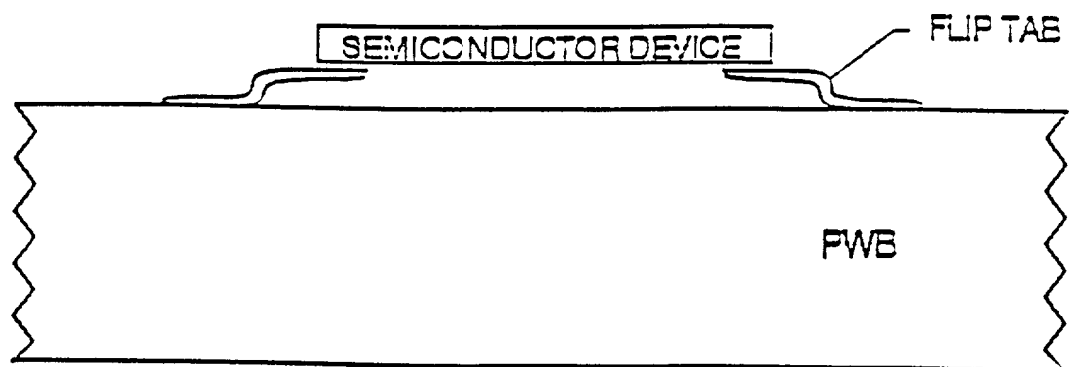


Fig. 3

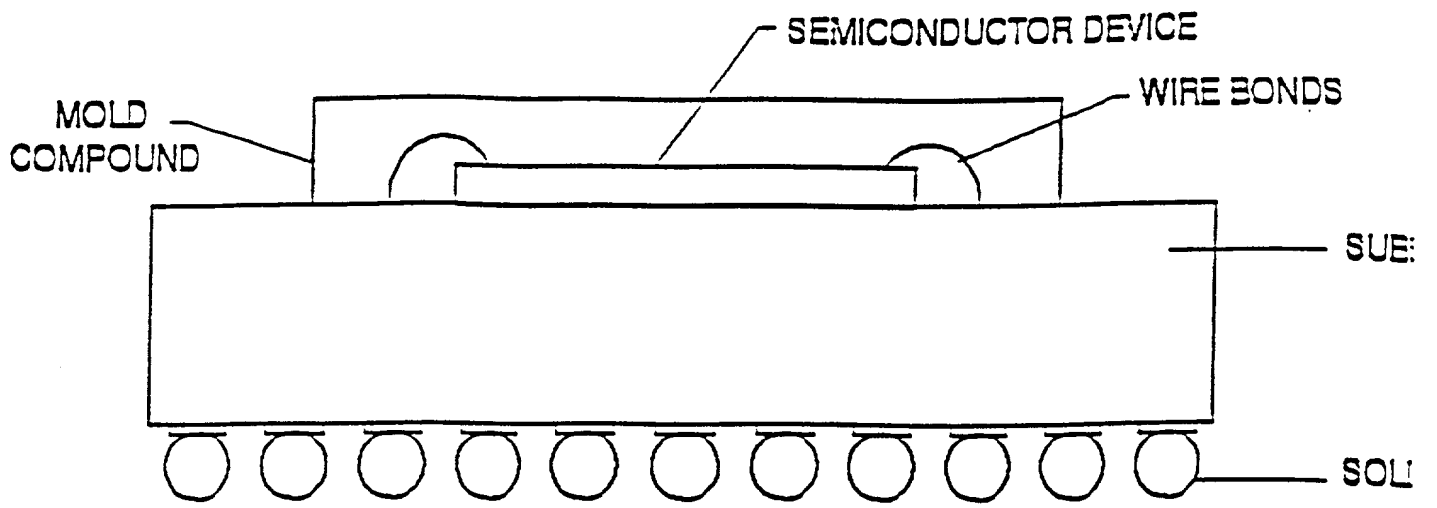


Fig. 4

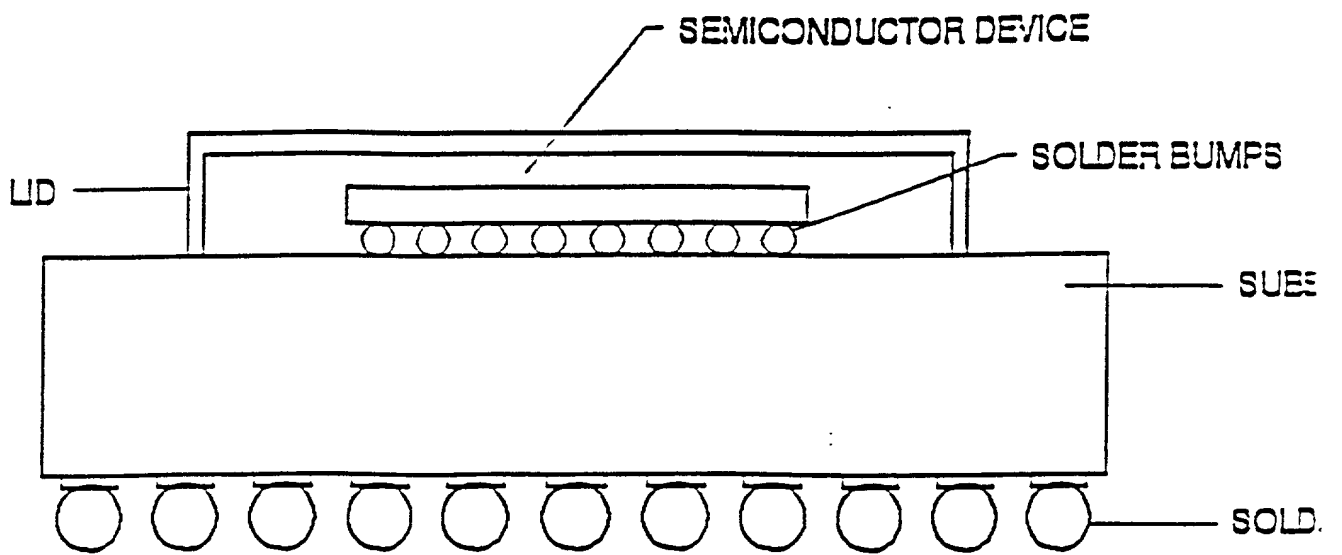


Fig. 5

PERIMETER

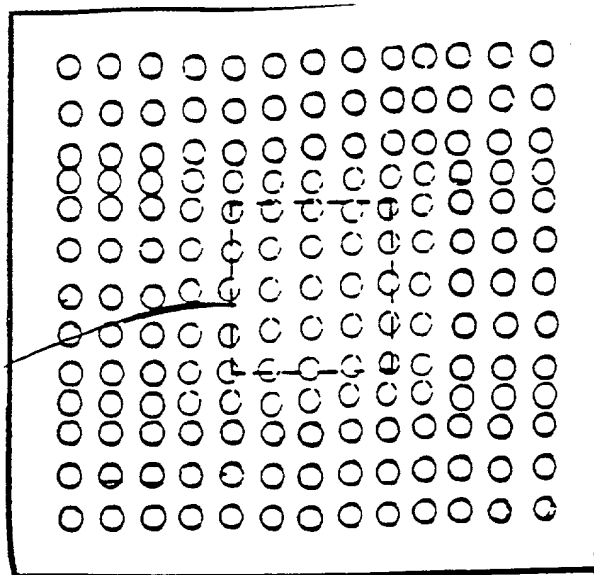


Fig. 6

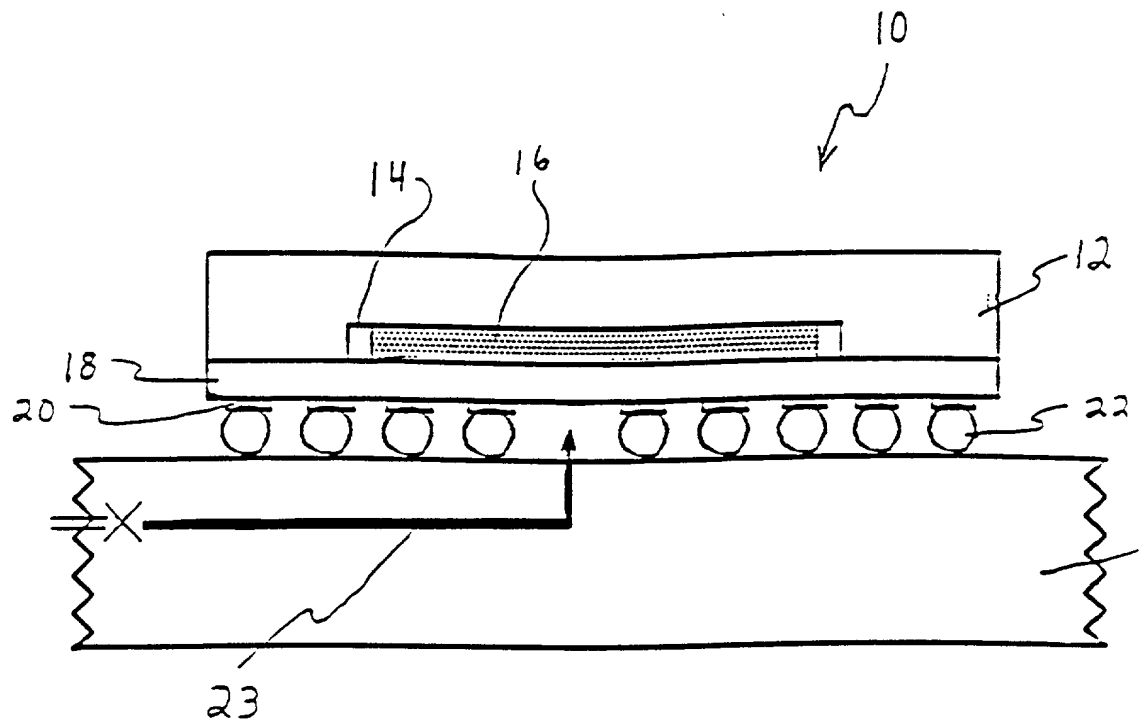


FIG. 7

FIG. 8

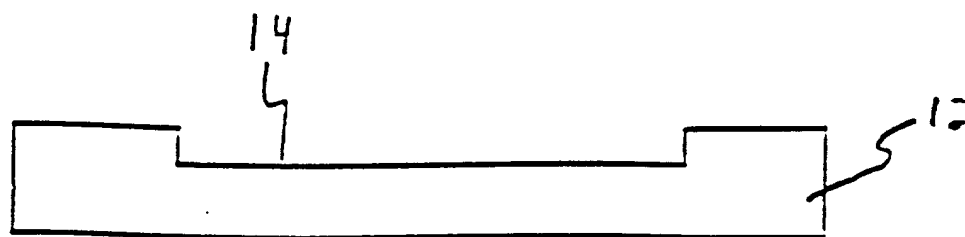


Fig. 8

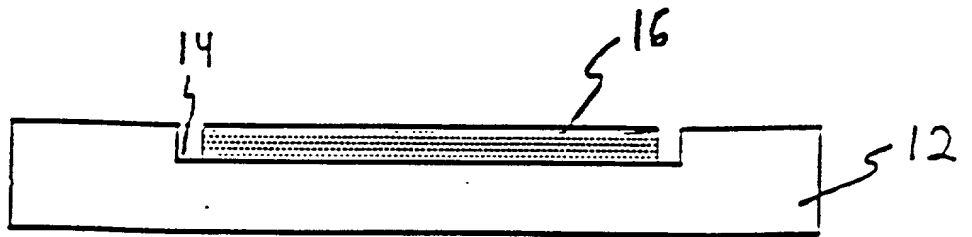


Fig. 9

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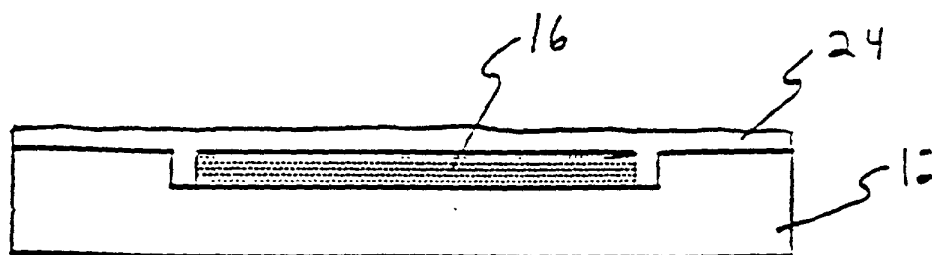


Fig. 10

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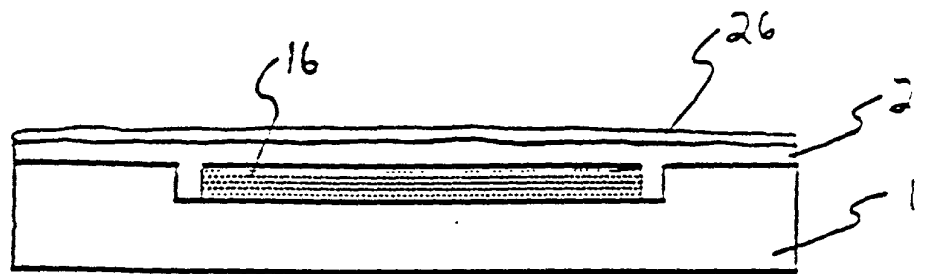


Fig. 11

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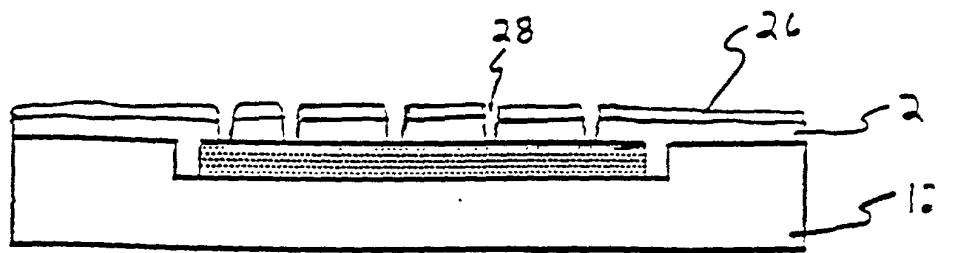


Fig. 12

Fig. 13

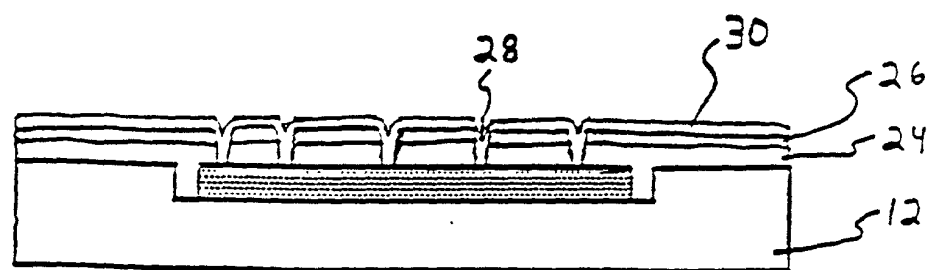


Fig. 13

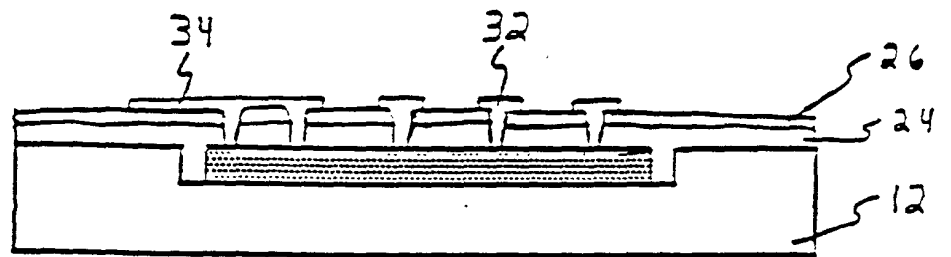
[illegible]

Fig. 14

A cross-sectional view of a multi-layered structure. It features a central cavity or channel. The structure is composed of several layers, with the top layer labeled 34, the middle layer labeled 32, and the bottom layer labeled 36. The cavity is defined by a bottom surface labeled 1 and side walls labeled 2. The bottom surface 1 is shown as a series of horizontal lines, indicating a textured or layered material. The side walls 2 are shown as vertical lines. The top layer 34 has a wavy, irregular top surface. The middle layer 32 has a flat top surface. The bottom layer 36 has a flat bottom surface. The cavity is formed by the bottom layer 36 and the side walls 2.

Fig. 15

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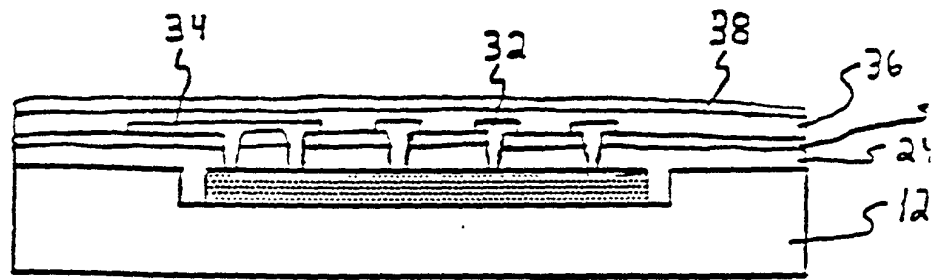


Fig. 16

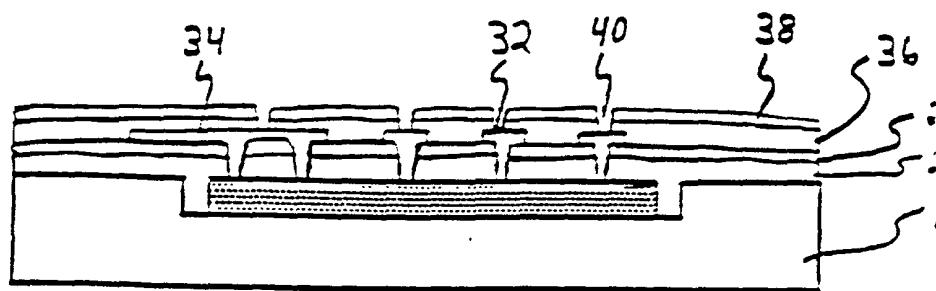
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Fig. 17

Fig. 18

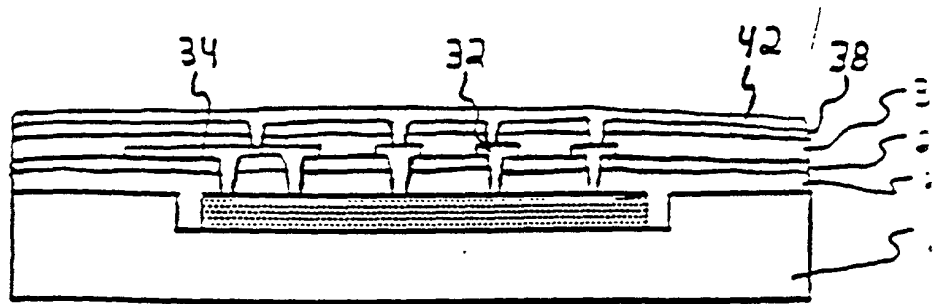


Fig. 18

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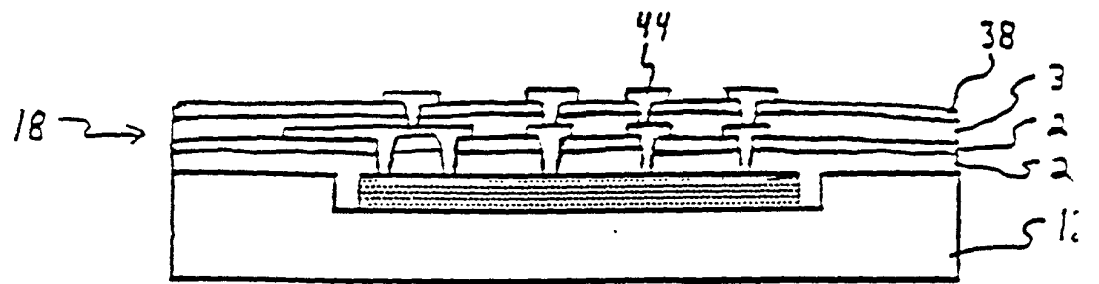


Fig. 19

A cross-sectional view of a semiconductor device. A central channel (12) is formed in a substrate. Above the channel, a series of gates (18) are formed, each with a top layer (46) and a bottom layer (36). The gates are separated by spacers (24). The device is covered by a top layer (38). The channel (12) is shown with a textured pattern, indicating a different material or doping.

Fig. 20

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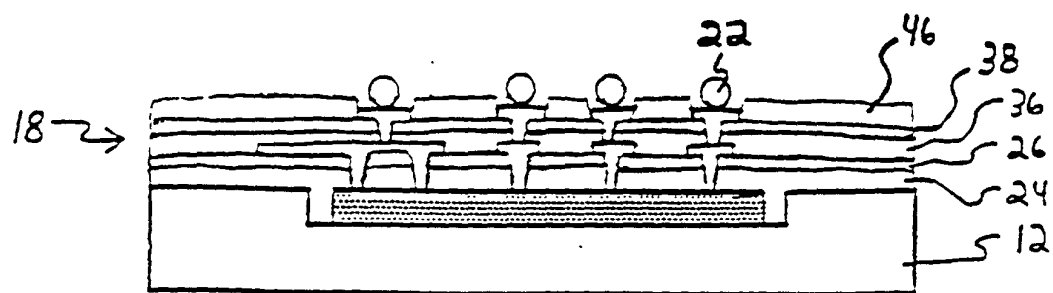


Fig. 21

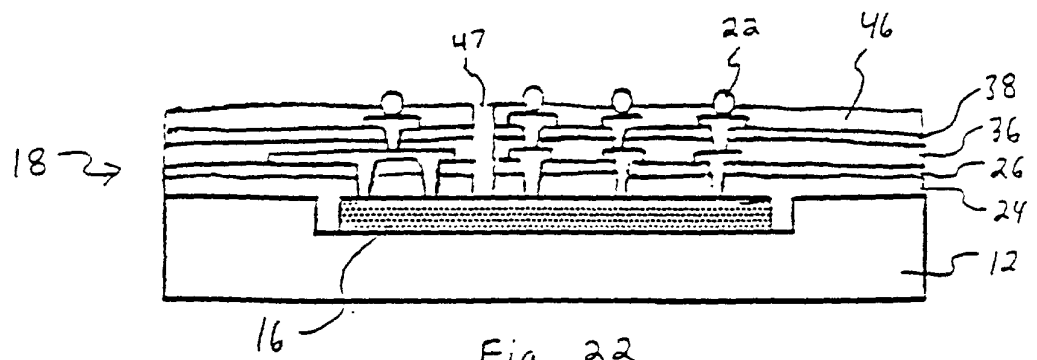


Fig. 22

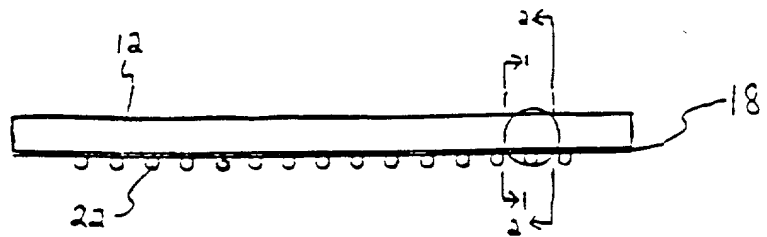


Fig. 23

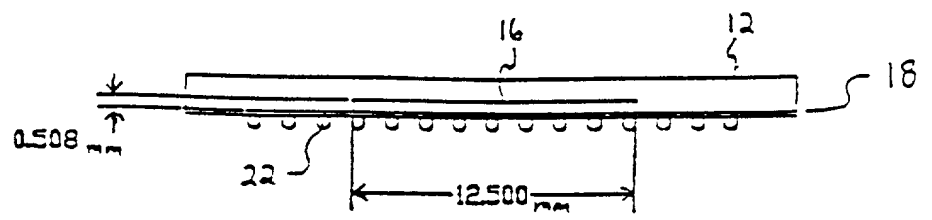


Fig. 24

TOP SECRET

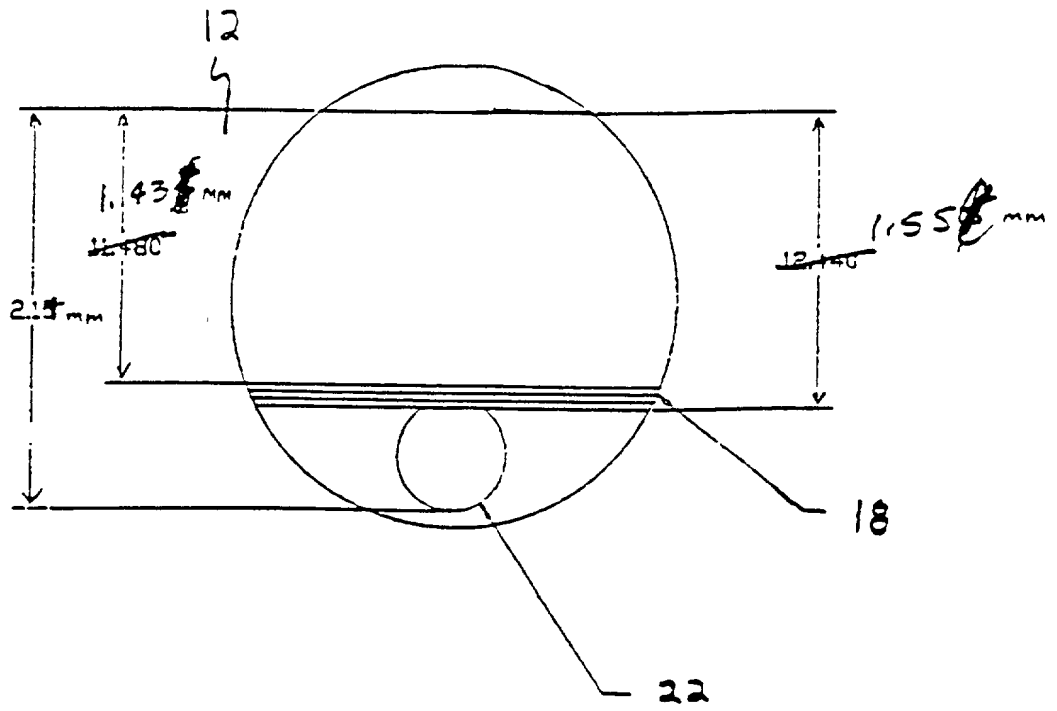


Fig. 25

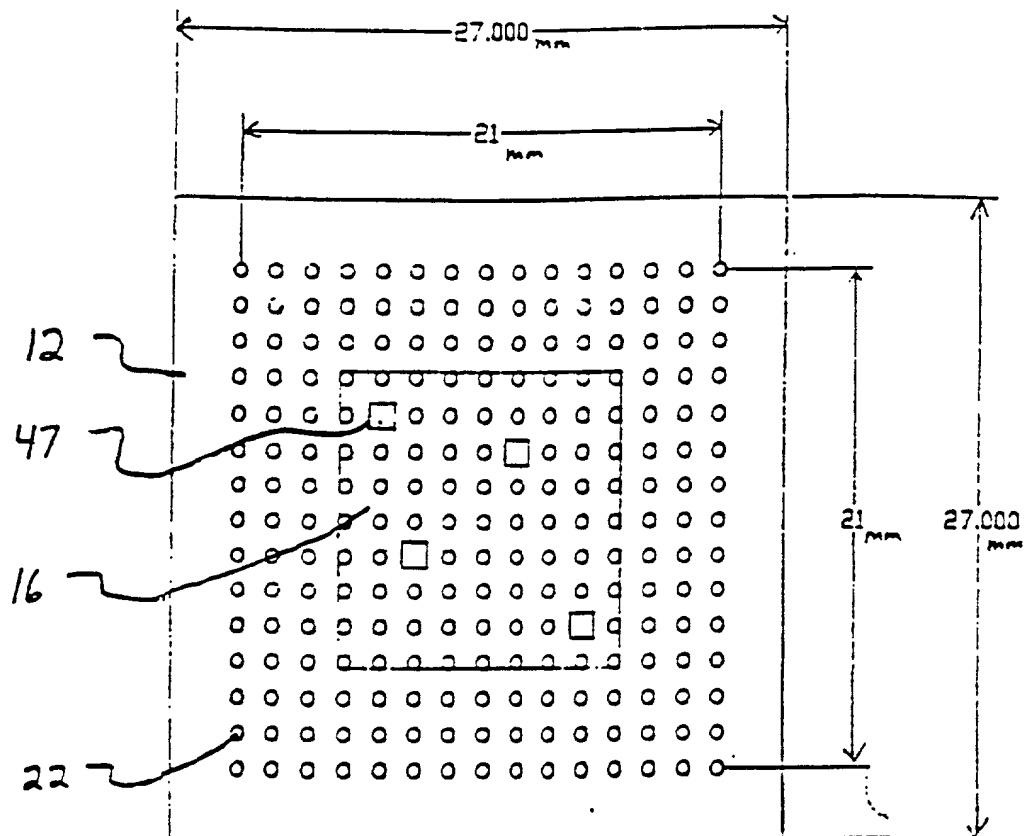


Fig. 26

FIG. 27

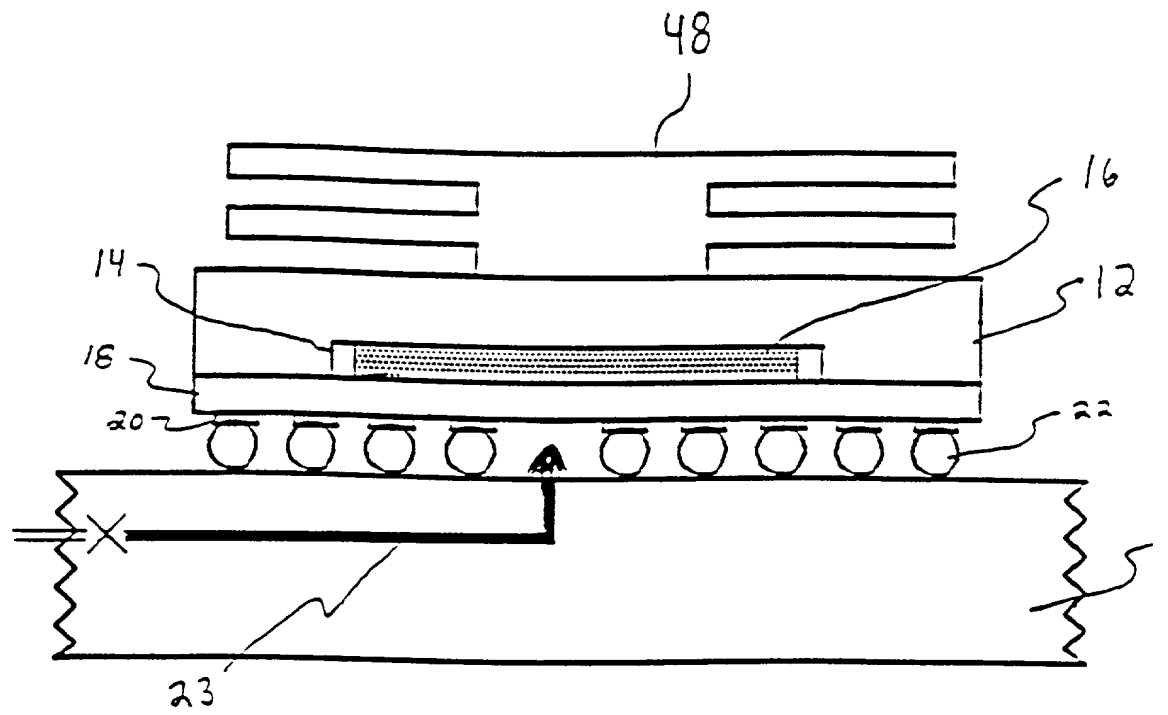


FIG. 27

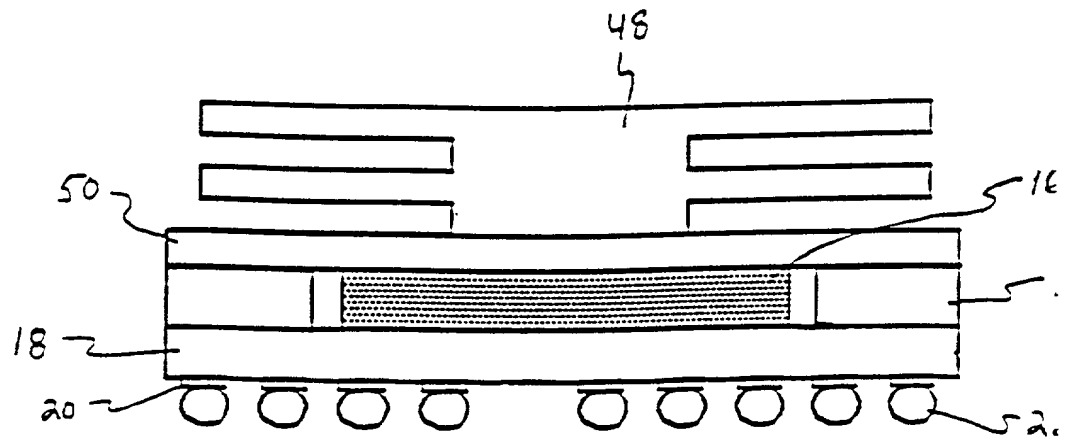


FIG. 28

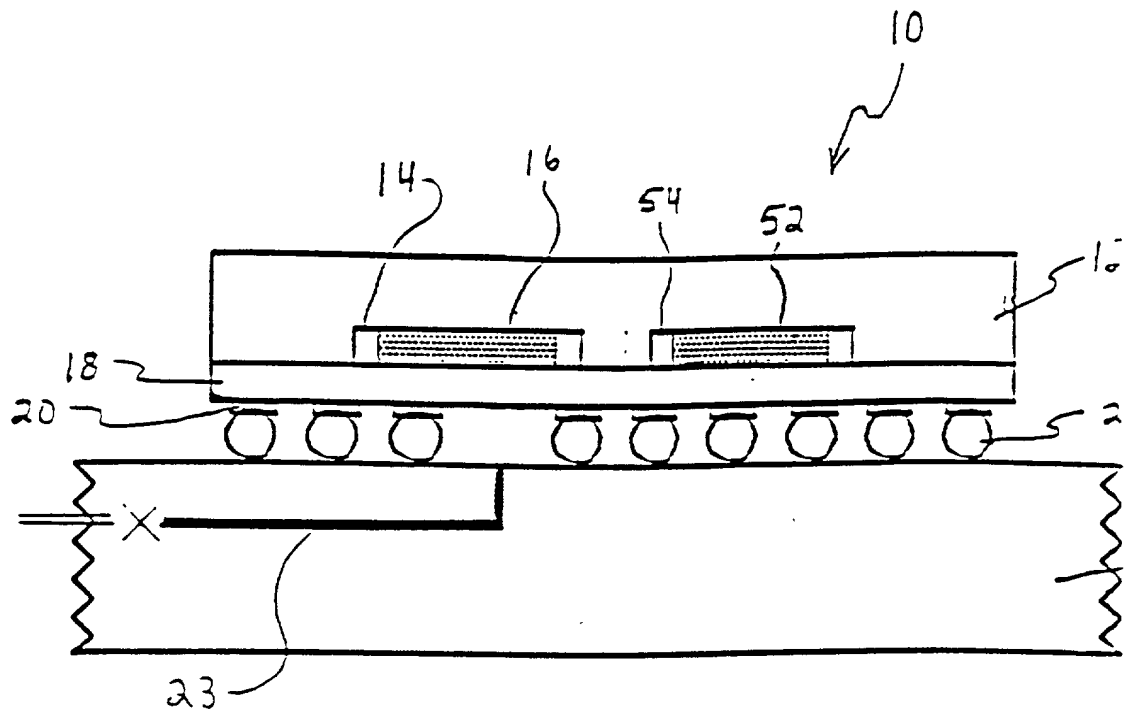


FIG. 29

TOP VIEW

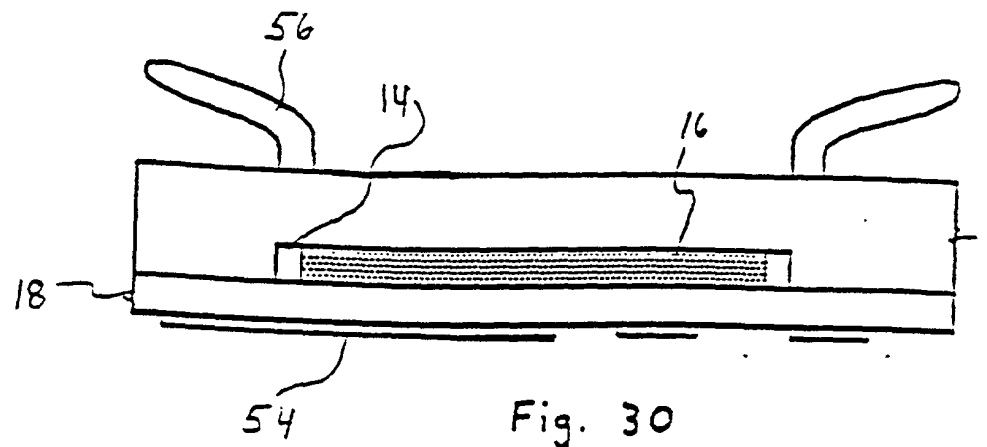


Fig. 30

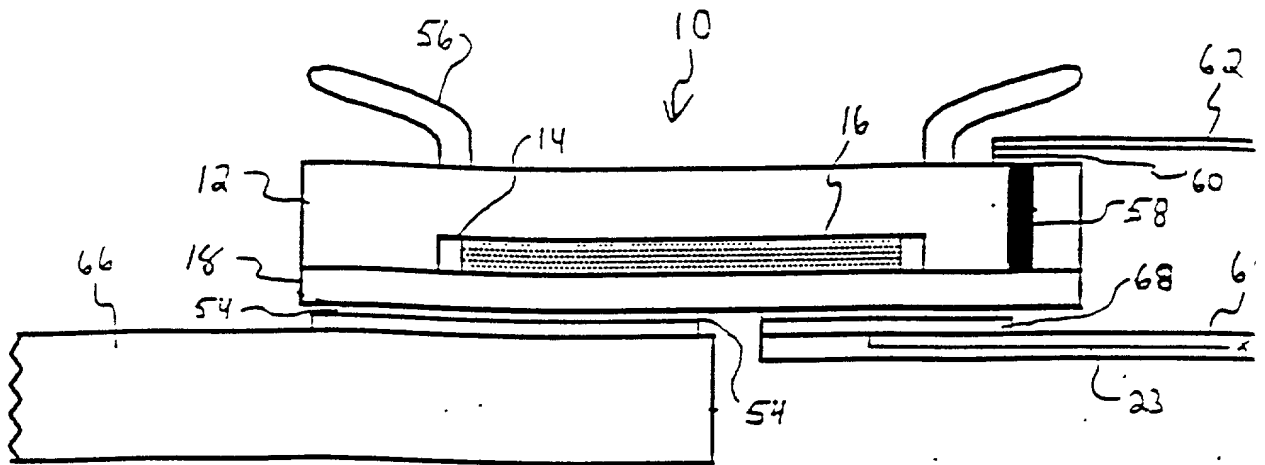


FIG. 31

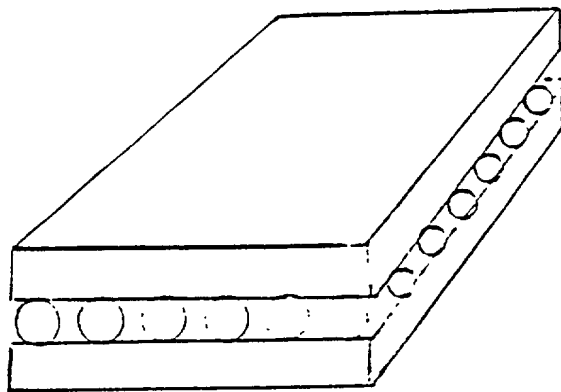


Fig. 32

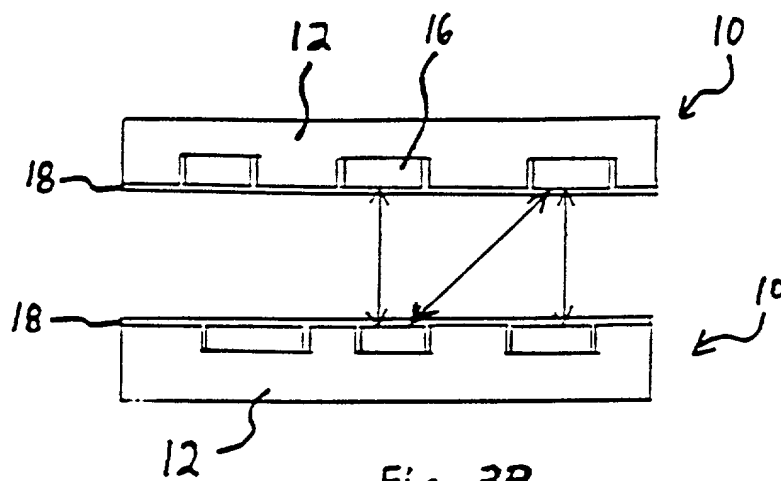


Fig. 39

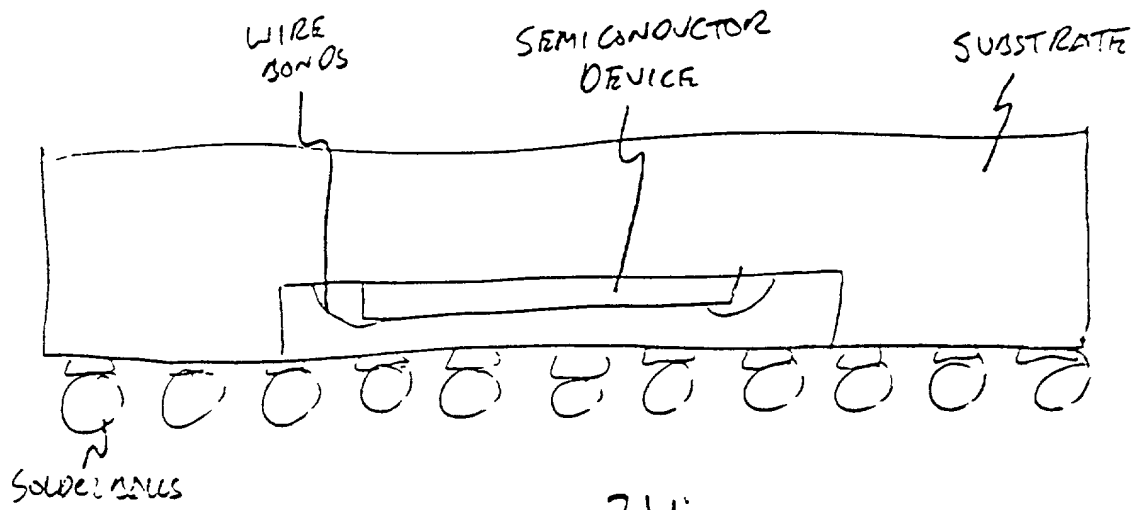


Figure 34

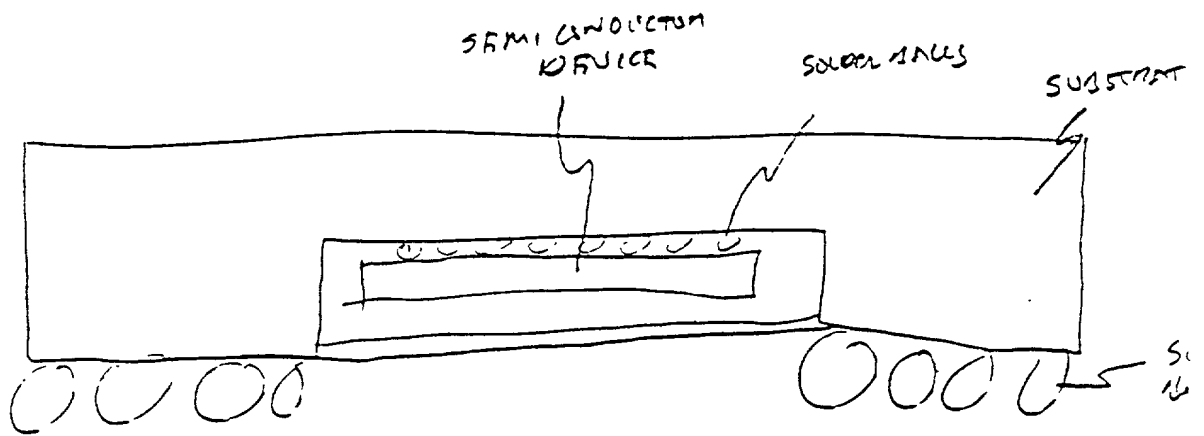


Fig 35